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The Cyclone V SoC development

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HPS SoC Boot Guide - Cyclone V  
SoC Development Kit : Design  
Example \ Outside Design Store:  
Cyclone V SoC Development Kit:

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Reference Manual  
Cyclone V: 15.0.0 : Intel: AN 717:  
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Board Kit See original listing.

Terasic Cyclone V DE1-SOC FPGA  
Development Board Kit:

Condition: Used. Ended: Sep 21,  
2020. Price: US \$165.00.

Shipping: Calculate Varies based  
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Item location: Rushville, Indiana,

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Edition, Version 20.1 includes

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functional and security updates. Users should keep their software up-to-date and follow the technical recommendations to help improve security. Additional security updates are planned and will be provided as they become available.

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Atlas-SoC (DE0-Nano-SoC)

Cyclone V: 16.0.0 : Terasic Atlas-  
SoC (DE0-Nano-SoC) Baseline

Pinout : Design Example: Atlas-  
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16.1.0 : Terasic Atlas-SoC

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